

AMENDMENTS TO THE SPECIFICATION

Page 10, fourth paragraph, delete in its entirety, and replace with the following:

Figure 3 is another possible example of the integrating circuit 20 of Figure 1. It shows a somewhat simplified version of the circuit diagram of Figure 2. In particular, this example does not ~~includes~~ include bypass capacitors 71, 72 and spare transistor MP31, and bias transistors MP22 and MP11 ~~is~~ are conceptually reduced to a single transistor MP22 (the two-transistor arrangement is used in Figure 2 because of layout considerations). The rest of the circuit is substantially similar to that of Figure 2 and will accordingly not be described again.